

<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: S&ZIO031002 Appl. No.: 10/690,001  Applicant: MARTIN BROX  Filing Date: October 21, 2003 Group Art Unit: 2819			
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EXAMINER INITIALS	A	PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
JR	A	6,140,841	10/31/00	Suh	326	60	/
JR	B	6,292,014 B1	9/18/01	Hedberg	326	30	
	C						
	D						
	E						
	F						
	G						
	H						
	I						

  

FOREIGN PATENT DOCUMENT							
	J	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
JR	J	198 25 258 A1	12/9/99	Germany			
	K						
	L						
	M						
	N						

  

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
JR		Author not listed: "EIA/JEDEC Standard; Stub Series Terminated Logic for 3.3 Volts (SSTL_3)", Electronic Industries Association, Engineering Department, No. 8-8, August 1996, pp. 1-14

  

EXAMINER <i>Janne n Cl</i>	DATE CONSIDERED 4/15/05
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	